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JEPG US 87 09/26/01		Class	Subclass	ISSUE CLASSIFICATION	PATENT NUMBER	
U.S. UTILITY Patent Application						
 SCANNED <i>Campbell</i> O.A. TC			O.R.P.E.		PATENT DATE	
APPLICATION NO.		CONT/PRIOR	CLASS <i>257</i>	SUBCLASS <i>534</i>	ART UNIT <i>2516</i>	EXAMINER <i>Wells</i>
09/811387		F				
APPLICANTS Masayuki Miyazaki Koichiro Ishibashi						
TITLE Semiconductor integrated circuit device						

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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	(Assistant Examiner) _____ (Date)				
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	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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